

ABSTRACT

A disk array device includes at least one channel control section for receiving data input/output requests from an information processing device, at least one disk control section for sending data input/output requests to hard disk drives based on the data input/output requests received by the channel control section, and a shared memory for the channel control section and the disk control section to read and write data. Information concerning ports that are actually used by the user is stored in a port control table in the shared memory, and the channel control section refers to the port control table and controls whether to make each port available for use by deciding whether to respond to a primitive sequence in a link initialization received from the information processing device regarding each of the plurality of ports.

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